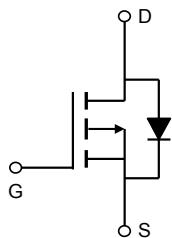
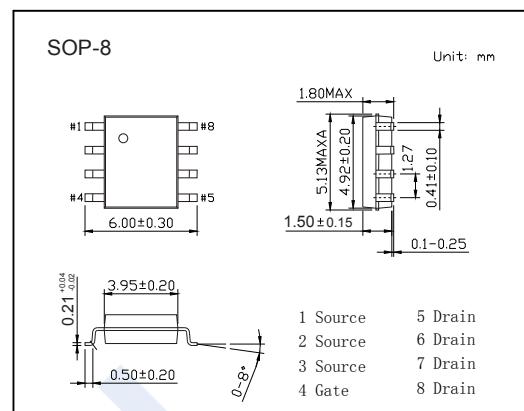


P-Channel MOSFET

AO4409 (KO4409)

■ Features

- V_{DS} (V) = -30V
- I_D = -15 A (V_{GS} = -10V)
- $R_{DS(ON)} < 7.5\text{m}\Omega$ (V_{GS} = -10V)
- $R_{DS(ON)} < 12\text{m}\Omega$ (V_{GS} = -4.5V)

■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	I_D	-15	A
		-12.8	
Pulsed Drain Current	I_{DM}	-80	
Avalanche Current	I_{AS}, I_{AR}	30	
Avalanche energy	E_{AS}, E_{AR}	135	mJ
Power Dissipation	P_D	3.1	W
		2	
Thermal Resistance.Junction- to-Ambient	R_{thJA}	40	$^\circ\text{C}/\text{W}$
		75	
Thermal Resistance.Junction- to-Lead	R_{thJL}	24	$^\circ\text{C}$
Junction Temperature	T_J	150	
Junction Storage Temperature Range	T_{stg}	-55 to 150	

P-Channel MOSFET

AO4409 (KO4409)

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V _{DSS}	I _D =-250 μ A, V _{GS} =0V	-30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V			-5	uA
		V _{DS} =-30V, V _{GS} =0V, T _J =55°C			-25	
Gate-Body leakage current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} I _D =-250 μ A	-1.4		-2.7	V
Static Drain-Source On-Resistance	R _{Ds(on)}	V _{GS} =-10V, I _D =-15A			7.5	mΩ
		V _{GS} =-10V, I _D =-15A T _J =125°C			11.5	
		V _{GS} =-4.5V, I _D =-10A			12	
On state drain current	I _{D(ON)}	V _{GS} =-10V, V _{DS} =-5V	-80			A
Forward Transconductance	g _{FS}	V _{DS} =-5V, I _D =-15A	35	50		S
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =-15V, f=1MHz		5270	6400	pF
Output Capacitance	C _{oss}			945		
Reverse Transfer Capacitance	C _{rss}			745		
Gate resistance	R _g	V _{GS} =0V, V _{DS} =0V, f=1MHz		2	3	Ω
Total Gate Charge (10V)	Q _g	V _{GS} =-10V, V _{DS} =-15V, I _D =-15A		100	120	nC
Total Gate Charge (4.5V)				51.5		
Gate Source Charge	Q _{gs}			14.5		
Gate Drain Charge	Q _{gd}			23		
Turn-On DelayTime	t _{d(on)}	V _{GS} =-10V, V _{DS} =-15V, R _L =1Ω, R _{GEN} =3Ω		14		ns
Turn-On Rise Time	t _r			16.5		
Turn-Off DelayTime	t _{d(off)}			76.5		
Turn-Off Fall Time	t _f			37.5		
Body Diode Reverse Recovery Time	t _{rr}	I _F =-15A, d _i /d _t =100A/us		36.7	45	nC
Body Diode Reverse Recovery Charge	Q _{rr}			28		
Maximum Body-Diode Continuous Current	I _s				-5	A
Diode Forward Voltage	V _{SD}	I _s =-1A, V _{GS} =0V			-1	V

Note : The static characteristics in Figures 1 to 6 are obtained using <300 μs pulses, duty cycle 0.5% max.

■ Marking

Marking	4409 KC****
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P-Channel MOSFET

AO4409 (KO4409)

■ Typical Characteristics

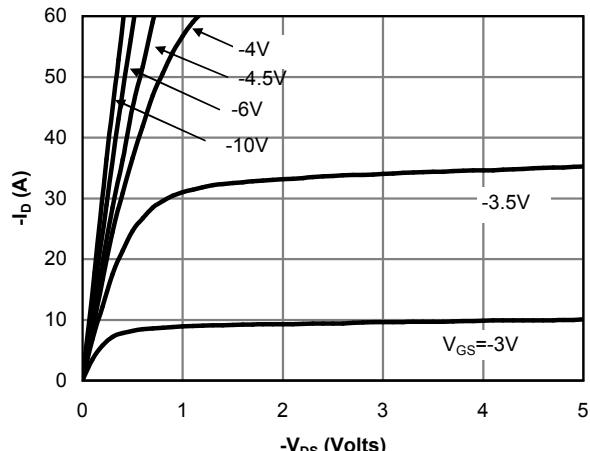


Fig 1: On-Region Characteristics (Note E)

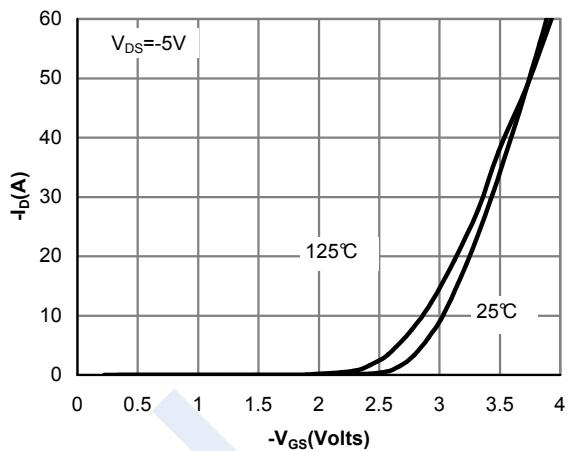


Figure 2: Transfer Characteristics (Note E)

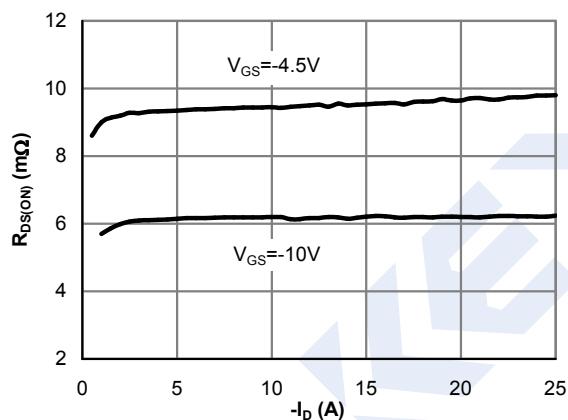


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

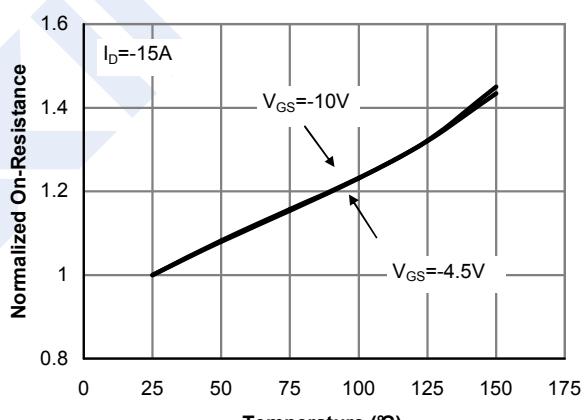


Figure 4: On-Resistance vs. Junction Temperature (Note E)

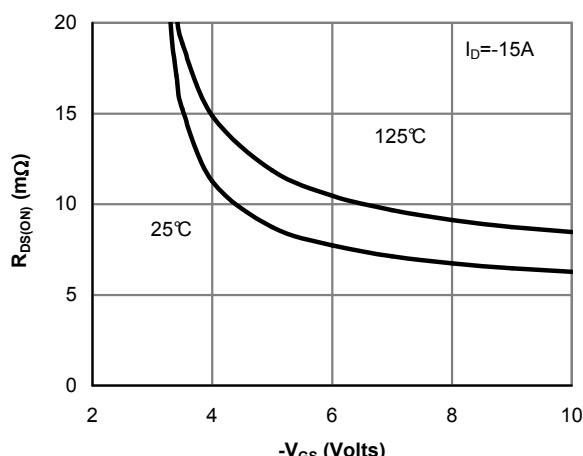


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

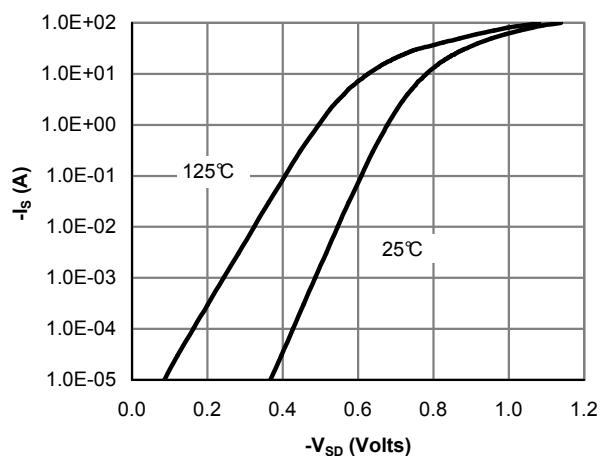


Figure 6: Body-Diode Characteristics (Note E)

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■ Typical Characteristics

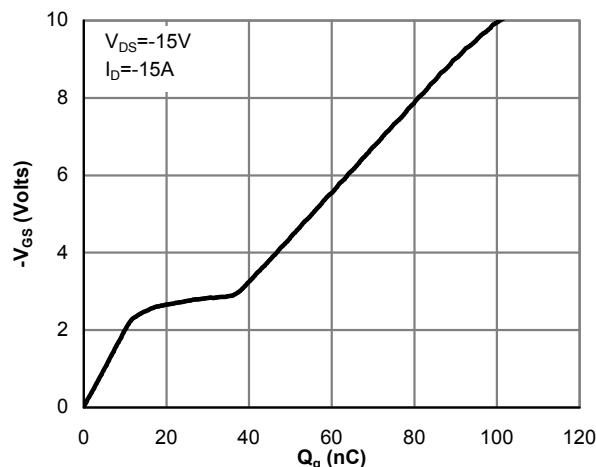


Figure 7: Gate-Charge Characteristics

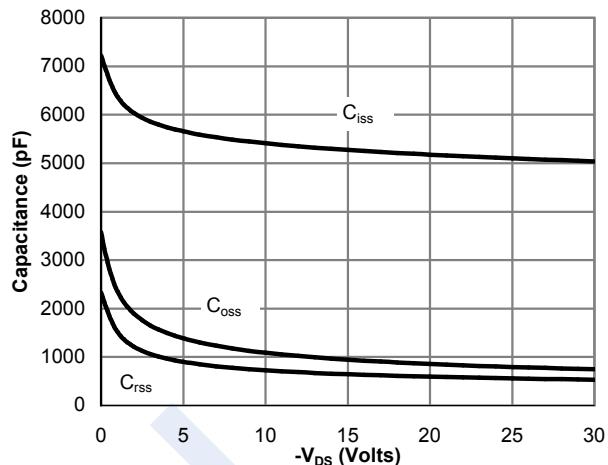


Figure 8: Capacitance Characteristics

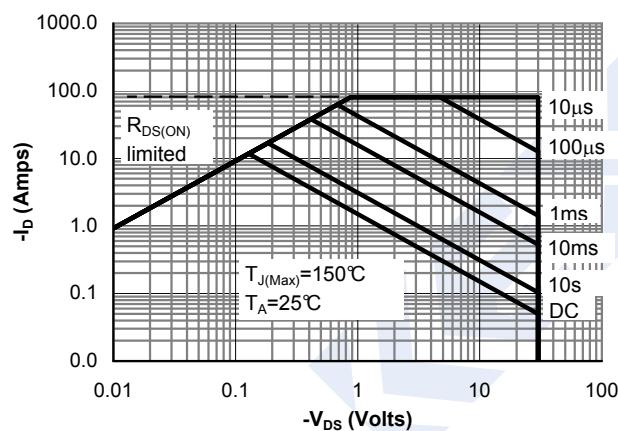


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

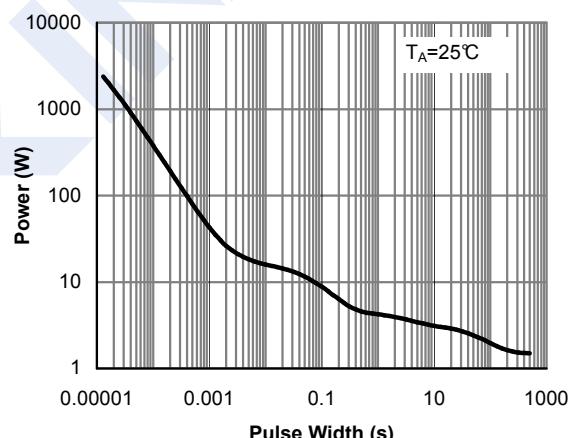


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

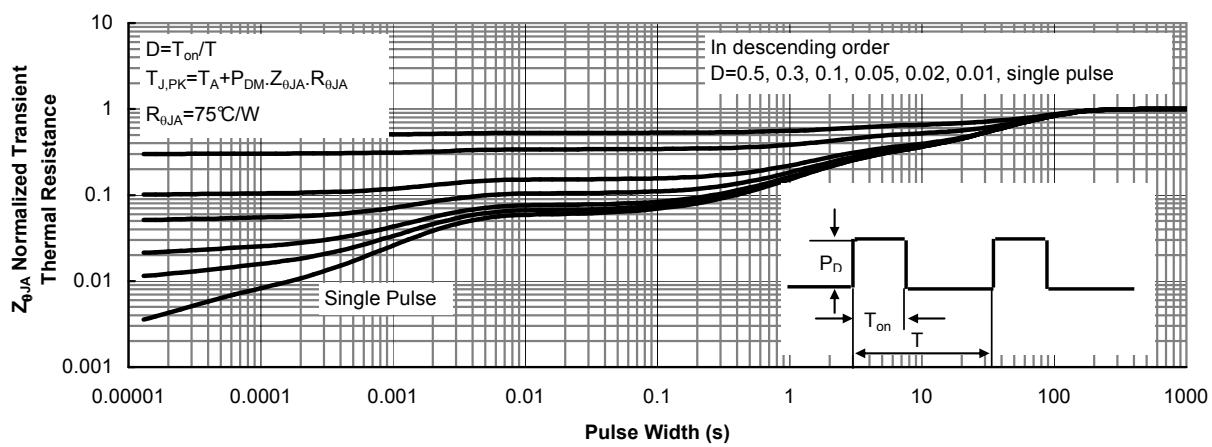


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)